

Chapter Fourteen

SEMICONDUCTOR ELECTRONICS

MATERIALS, DEVICES AND SIMPLE CIRCUITS



MCQ I

14.1 The conductivity of a semiconductor increases with increase in temperature because

- (a) number density of free current carriers increases.
- (b) relaxation time increases.
- (c) both number density of carriers and relaxation time increase.
- (d) number density of current carriers increases, relaxation time decreases but effect of decrease in relaxation time is much less than increase in number density.

14.2 In Fig. 14.1, V_o is the potential barrier across a p-n junction, when no battery is connected across the junction

- (a) 1 and 3 both correspond to forward bias of junction
- (b) 3 corresponds to forward bias of junction and 1 corresponds to reverse bias of junction
- (c) 1 corresponds to forward bias and 3 corresponds to reverse bias of junction.

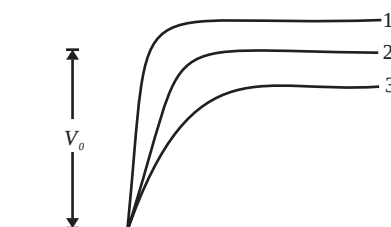


Fig. 14.1

(d) 3 and 1 both correspond to reverse bias of junction.

14.3 In Fig. 14.2, assuming the diodes to be ideal,

- (a) D_1 is forward biased and D_2 is reverse biased and hence current flows from A to B
- (b) D_2 is forward biased and D_1 is reverse biased and hence no current flows from B to A and vice versa.
- (c) D_1 and D_2 are both forward biased and hence current flows from A to B.
- (d) D_1 and D_2 are both reverse biased and hence no current flows from A to B and vice versa.

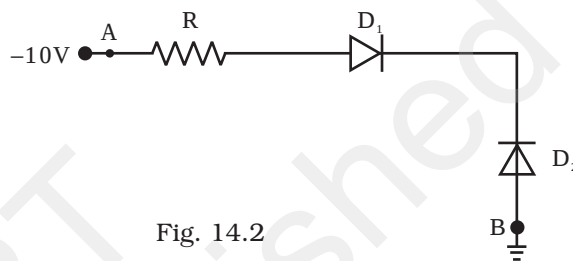


Fig. 14.2

14.4 A 220 V A.C. supply is connected between points A and B (Fig. 14.3). What will be the potential difference V across the capacitor?

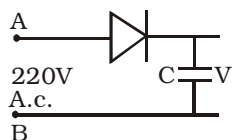


Fig. 14.3

- (a) 220V.
- (b) 110V.
- (c) 0V.
- (d) $220\sqrt{2}$ V.

14.5 Hole is

- (a) an anti-particle of electron.
- (b) a vacancy created when an electron leaves a covalent bond.
- (c) absence of free electrons.
- (d) an artificially created particle.

14.6 The output of the given circuit in Fig. 14.4.

- (a) would be zero at all times.



Fig. 14.4

- (b) would be like a half wave rectifier with positive cycles in output.
- (c) would be like a half wave rectifier with negative cycles in output.
- (d) would be like that of a full wave rectifier.

14.7 In the circuit shown in Fig. 14.5, if the diode forward voltage drop is 0.3 V, the voltage difference between A and B is

- (a) 1.3 V
- (b) 2.3 V
- (c) 0
- (d) 0.5 V

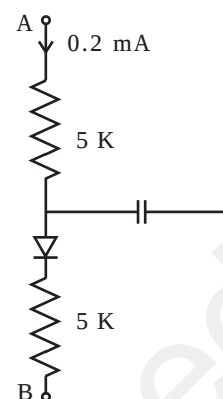


Fig. 14.5

14.8 Truth table for the given circuit (Fig. 14.6) is

(a)

A	B	E
0	0	1
0	1	0
1	0	1
1	1	0

(b)

A	B	E
0	0	1
0	1	0
1	0	0
1	1	1

(c)

A	B	E
0	0	0
0	1	1
1	0	0
1	1	1

(d)

A	B	E
0	0	0
0	1	1
1	0	1
1	1	0

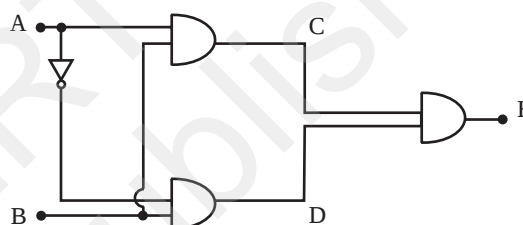


Fig. 14.6

MCQ II

14.9 When an electric field is applied across a semiconductor

- (a) electrons move from lower energy level to higher energy level in the conduction band.
- (b) electrons move from higher energy level to lower energy level in the conduction band.
- (c) holes in the valence band move from higher energy level to lower energy level.
- (d) holes in the valence band move from lower energy level to higher energy level.

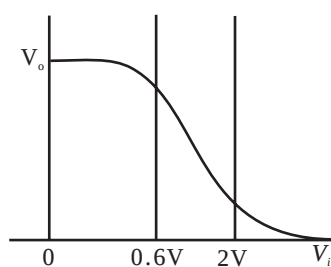


Fig. 14.7

14.10 Consider an npn transistor with its base-emitter junction forward biased and collector base junction reverse biased. Which of the following statements are true?.

- (a) Electrons crossover from emitter to collector.
- (b) Holes move from base to collector.
- (c) Electrons move from emitter to base.
- (d) Electrons from emitter move out of base without going to the collector.

14.11 Figure 14.7 shows the transfer characteristics of a base biased CE transistor. Which of the following statements are true?

- (a) At $V_i = 0.4V$, transistor is in active state.
- (b) At $V_i = 1V$, it can be used as an amplifier.
- (c) At $V_i = 0.5V$, it can be used as a switch turned off.
- (d) At $V_i = 2.5V$, it can be used as a switch turned on.

14.12 In a npn transistor circuit, the collector current is 10mA. If 95 per cent of the electrons emitted reach the collector, which of the following statements are true?

- (a) The emitter current will be 8 mA.
- (b) The emitter current will be 10.53 mA.
- (c) The base current will be 0.53 mA.
- (d) The base current will be 2 mA.

14.13 In the depletion region of a diode

- (a) there are no mobile charges
- (b) equal number of holes and electrons exist, making the region neutral.
- (c) recombination of holes and electrons has taken place.
- (d) immobile charged ions exist.

14.14 What happens during regulation action of a Zener diode?

- (a) The current in and voltage across the Zenor remains fixed.
- (b) The current through the series Resistance (R_s) changes.
- (c) The Zener resistance is constant.
- (d) The resistance offered by the Zener changes.

14.15 To reduce the ripples in a rectifier circuit with capacitor filter

- (a) R_L should be increased.
- (b) input frequency should be decreased.
- (c) input frequency should be increased.
- (d) capacitors with high capacitance should be used.

- 14.16** The breakdown in a reverse biased p-n junction diode is more likely to occur due to
- large velocity of the minority charge carriers if the doping concentration is small.
 - large velocity of the minority charge carriers if the doping concentration is large.
 - strong electric field in a depletion region if the doping concentration is small.
 - strong electric field in the depletion region if the doping concentration is large.

VSA

- 14.17** Why are elemental dopants for Silicon or Germanium usually chosen from group XIII or group XV?
- 14.18** Sn, C, and Si, Ge are all group XIV elements. Yet, Sn is a conductor, C is an insulator while Si and Ge are semiconductors. Why?
- 14.19** Can the potential barrier across a p-n junction be measured by simply connecting a voltmeter across the junction?
- 14.20** Draw the output waveform across the resistor (Fig. 14.8).

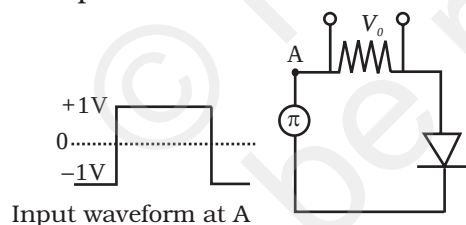


Fig. 14.8

- 14.21** The amplifiers X, Y and Z are connected in series. If the voltage gains of X, Y and Z are 10, 20 and 30, respectively and the input signal is 1 mV peak value, then what is the output signal voltage (peak value)
- if dc supply voltage is 10V?
 - if dc supply voltage is 5V?
- 14.22** In a CE transistor amplifier there is a current and voltage gain associated with the circuit. In other words there is a power gain. Considering power a measure of energy, does the circuit violate conservation of energy?

S.A

14.23

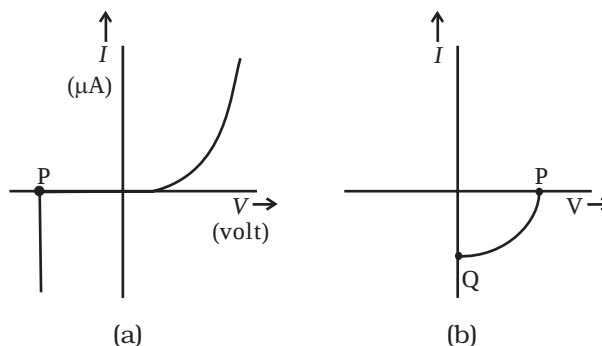


Fig. 14.9

- Name the type of a diode whose characteristics are shown in Fig. 14.9 (A) and Fig. 14.9(B).
- What does the point P in Fig. (A) represent?
- What does the points P and Q in Fig. (B) represent?

14.24 Three photo diodes D1, D2 and D3 are made of semiconductors having band gaps of 2.5eV, 2eV and 3eV, respectively. Which ones will be able to detect light of wavelength $6000 \text{ \AA}$?

14.25 If the resistance R_1 is increased (Fig. 14.10), how will the readings of the ammeter and voltmeter change?

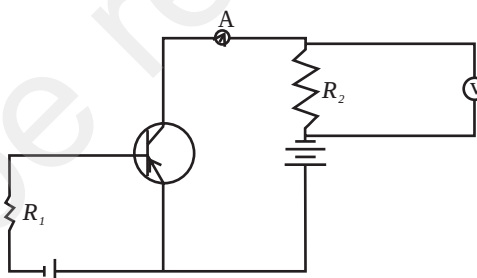


Fig. 14.10

- Two car garages have a common gate which needs to open automatically when a car enters either of the garages or cars enter both. Devise a circuit that resembles this situation using diodes for this situation.
- How would you set up a circuit to obtain NOT gate using a transistor?
- Explain why elemental semiconductor cannot be used to make visible LEDs.

14.29 Write the truth table for the circuit shown in Fig. 14.11.
Name the gate that the circuit resembles.

14.30 A Zener of power rating 1 W is to be used as a voltage regulator. If zener has a breakdown of 5V and it has to regulate voltage which fluctuated between 3V and 7V, what should be the value of R_s for safe operation (Fig. 14.12)?

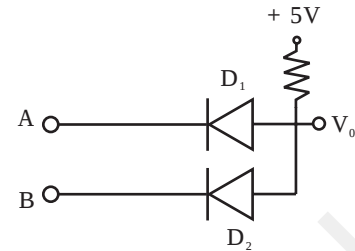


Fig. 14.11

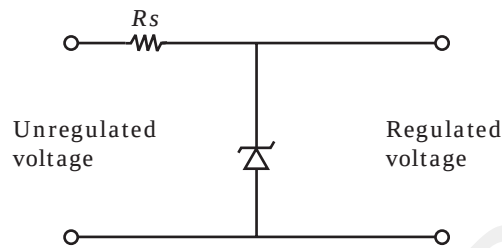


Fig. 14.12

LA

14.31 If each diode in Fig. 14.13 has a forward bias resistance of 25Ω and infinite resistance in reverse bias, what will be the values of the current I_1 , I_2 , I_3 and I_4 ?

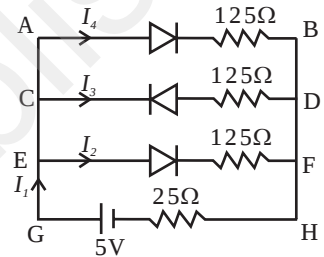


Fig. 14.13

14.32 In the circuit shown in Fig. 14.14, when the input voltage of the base resistance is 10V, V_{be} is zero and V_{ce} is also zero. Find the values of I_b , I_c and β .

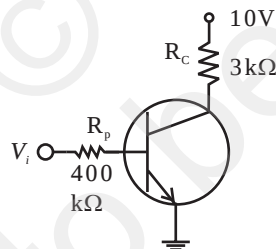


Fig. 14.14

14.33 Draw the output signals C_1 and C_2 in the given combination of gates (Fig. 14.15).

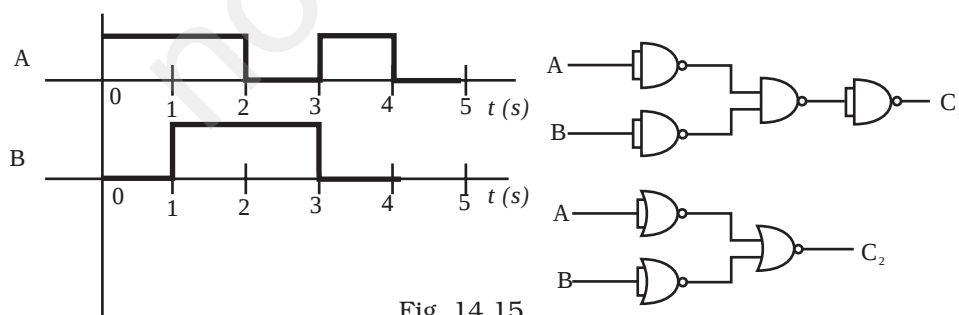


Fig. 14.15

- 14.34** Consider the circuit arrangement shown in Fig 14.16 (a) for studying input and output characteristics of npn transistor in CE configuration.

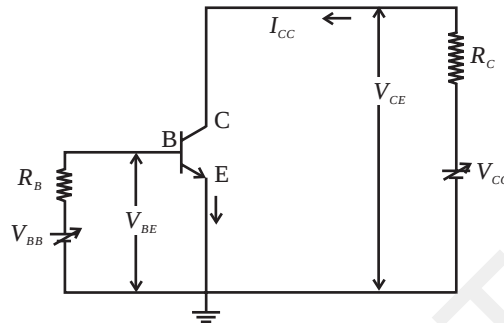


Fig. 14.16 (a)

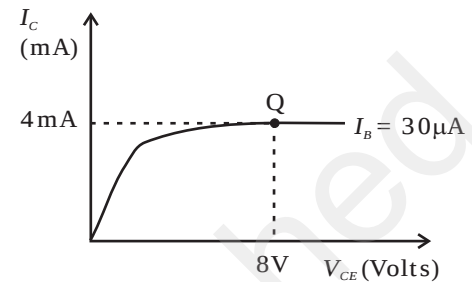


Fig. 14.16 (b)

Select the values of R_B and R_C for a transistor whose $V_{BE} = 0.7\text{ V}$, so that the transistor is operating at point Q as shown in the characteristics shown in Fig. 14.16 (b).

Given that the input impedance of the transistor is very small and $V_{CC} = V_{BB} = 16\text{ V}$, also find the voltage gain and power gain of circuit making appropriate assumptions.

- 14.35** Assuming the ideal diode, draw the output waveform for the circuit given in Fig. 14.17. Explain the waveform.

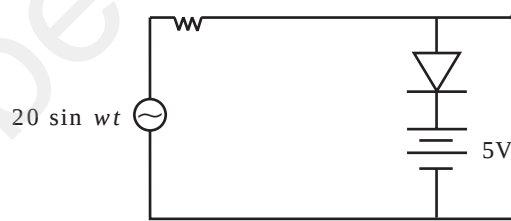


Fig. 14.17

- 14.36** Suppose a 'n'-type wafer is created by doping Si crystal having 5×10^{28} atoms/ m^3 with 1ppm concentration of As. On the surface 200 ppm Boron is added to create 'P' region in this wafer. Considering $n_i = 1.5 \times 10^{16} \text{ m}^{-3}$, (i) Calculate the densities of the charge carriers in the n & p regions. (ii) Comment which charge carriers would contribute largely for the reverse saturation current when diode is reverse biased.

14.37 An X-OR gate has following truth table:

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

It is represented by following logic relation

$$Y = \bar{A}.B + A.\bar{B}$$

Build this gate using AND, OR and NOT gates.

14.38 Consider a box with three terminals on top of it as shown in Fig. 14.18 (a):

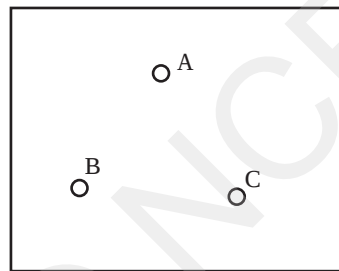


Fig. 14.18 (a)

Three components namely, two germanium diodes and one resistor are connected across these three terminals in some arrangement.

A student performs an experiment in which any two of these three terminals are connected in the circuit shown in Fig. 14.18 (b).

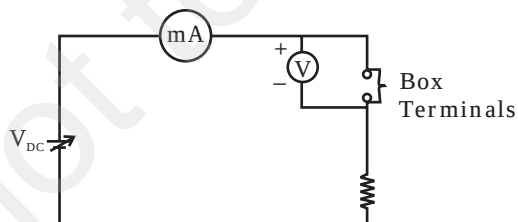


Fig. 14.18 (b)

The student obtains graphs of current-voltage characteristics for unknown combination of components between the two terminals connected in the circuit.

The graphs are

(i) when A is positive and B is negative

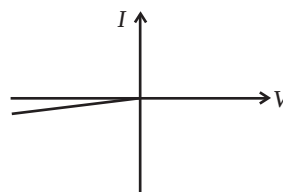


Fig. 14.18 (c)

(ii) when A is negative and B is positive

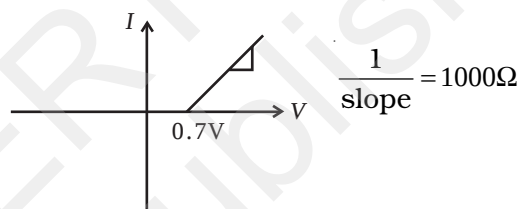


Fig. 14.18 (d)

(iii) When B is negative and C is positive

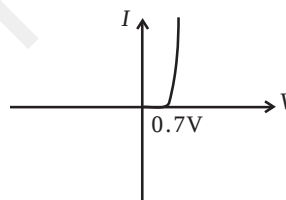


Fig. 14.18 (e)

(iv) When B is positive and C is negative

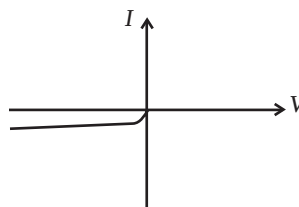


Fig. 14.18 (f)

(v) When A is positive and C is negative

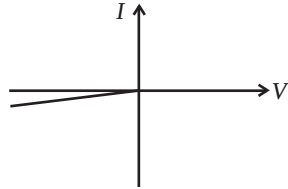


Fig. 14.18 (g)

(vi) When A is negative and C is positive

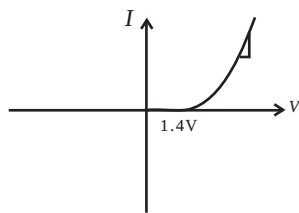


Fig. 14.18 (h)

From these graphs of current – voltage characteristic shown in Fig. 14.18 (c) to (h), determine the arrangement of components between A, B and C.

14.39 For the transistor circuit shown in Fig. 14.19, evaluate V_E , R_B , R_E given $I_C = 1 \text{ mA}$, $V_{CE} = 3 \text{ V}$, $V_{BE} = 0.5 \text{ V}$ and $V_{CC} = 12 \text{ V}$, $\beta = 100$.

14.40 In the circuit shown in Fig. 14.20, find the value of R_C .

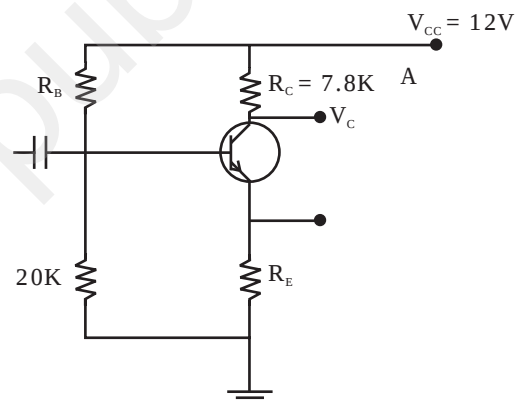


Fig. 14.19

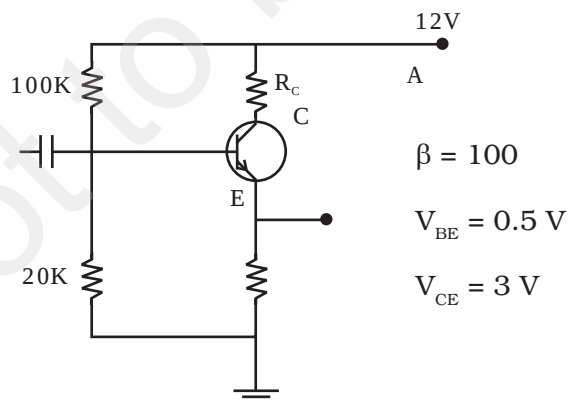


Fig. 14.20